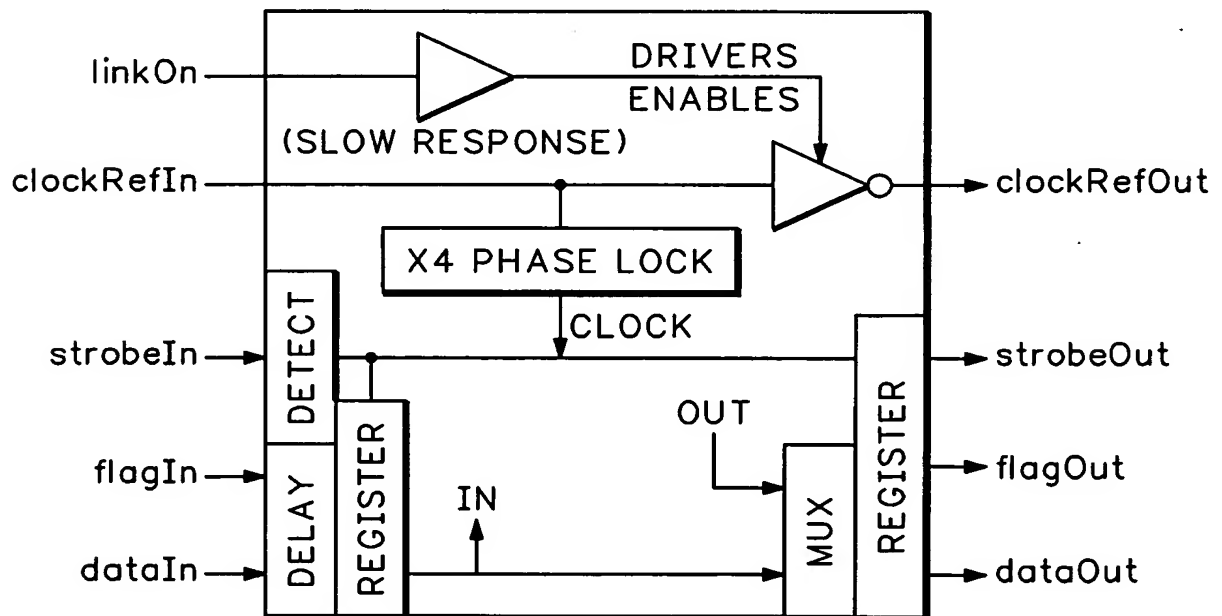


**FIG. 1**  
(PRIOR ART)



**FIG. 2**  
(PRIOR ART)

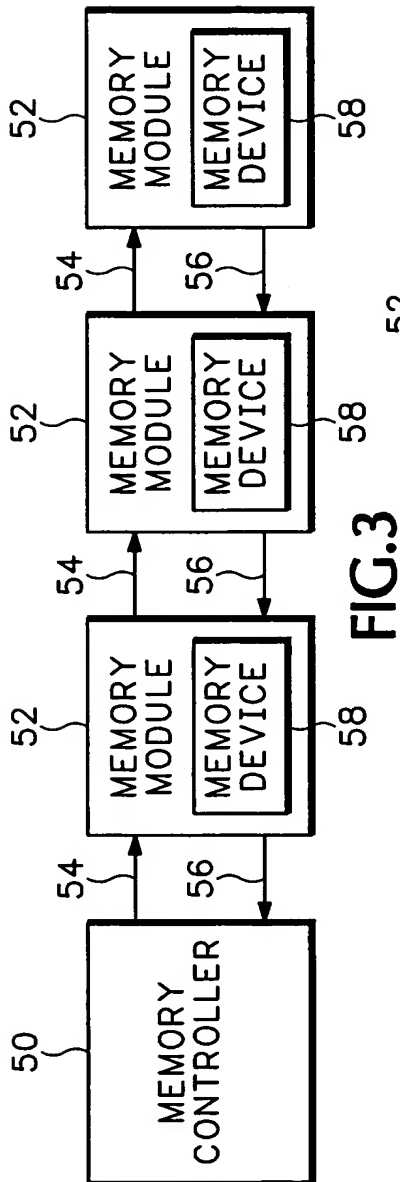


FIG. 3

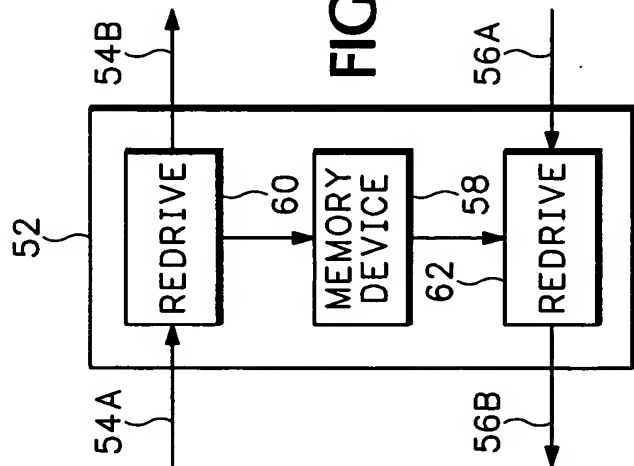


FIG. 4

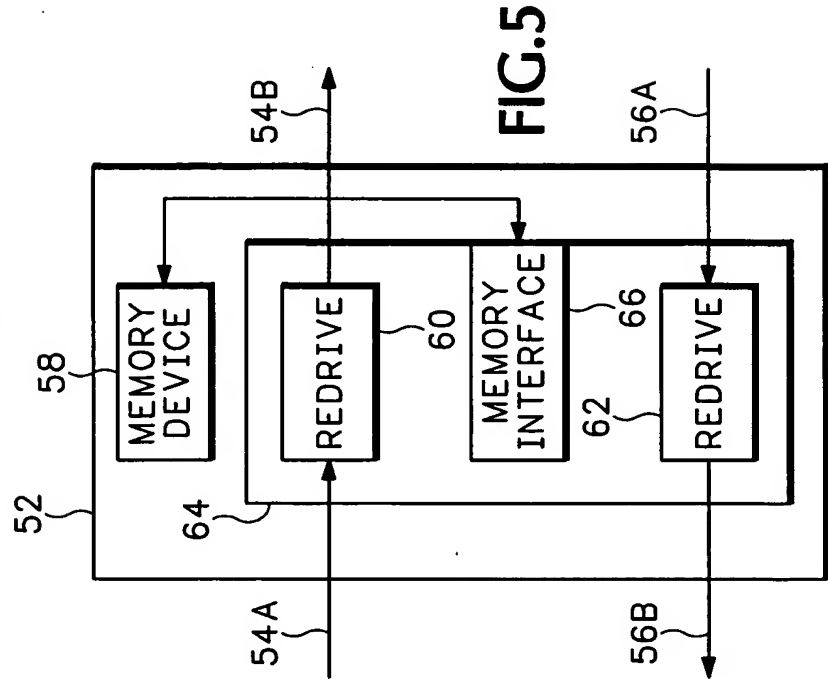
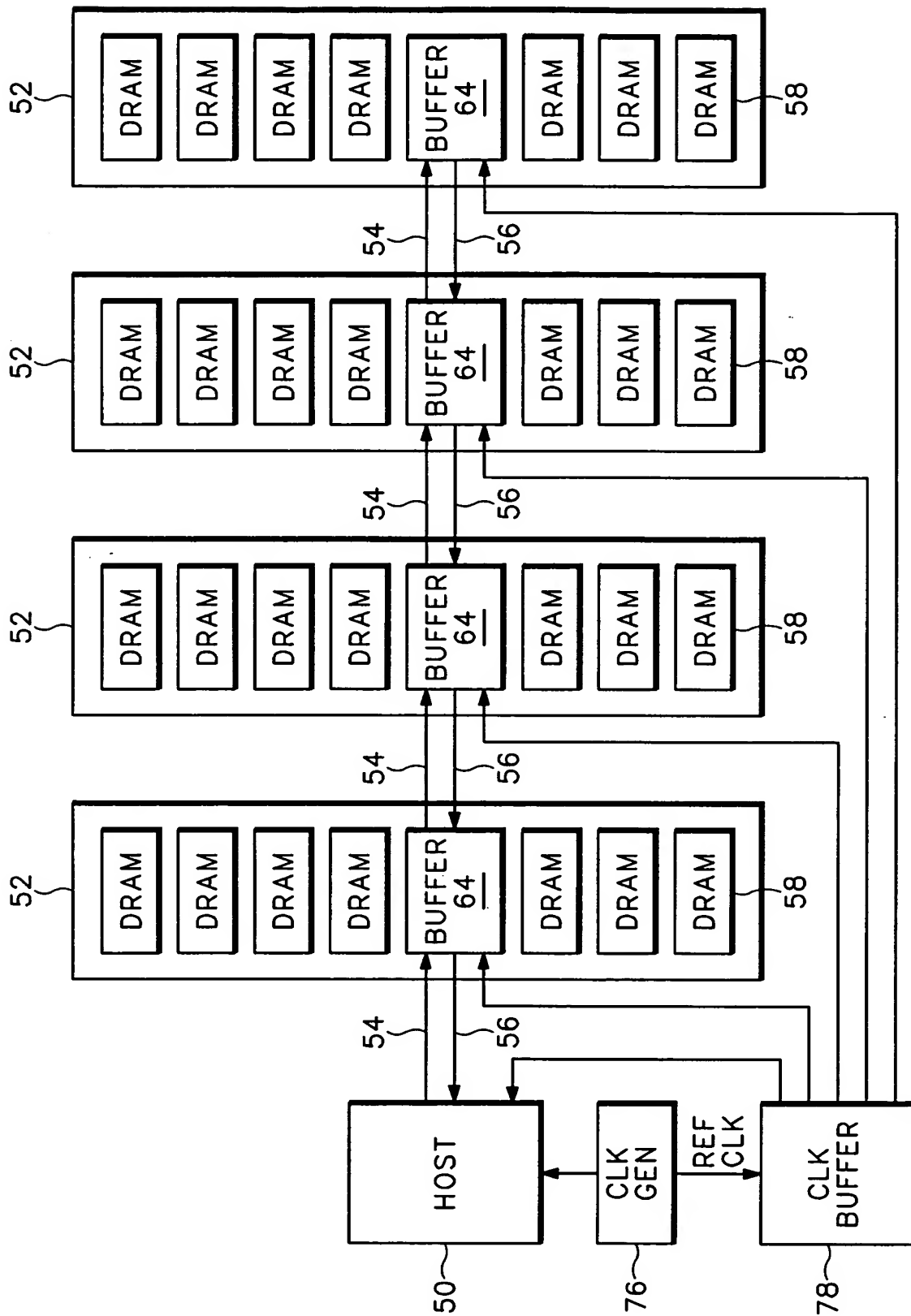


FIG. 5



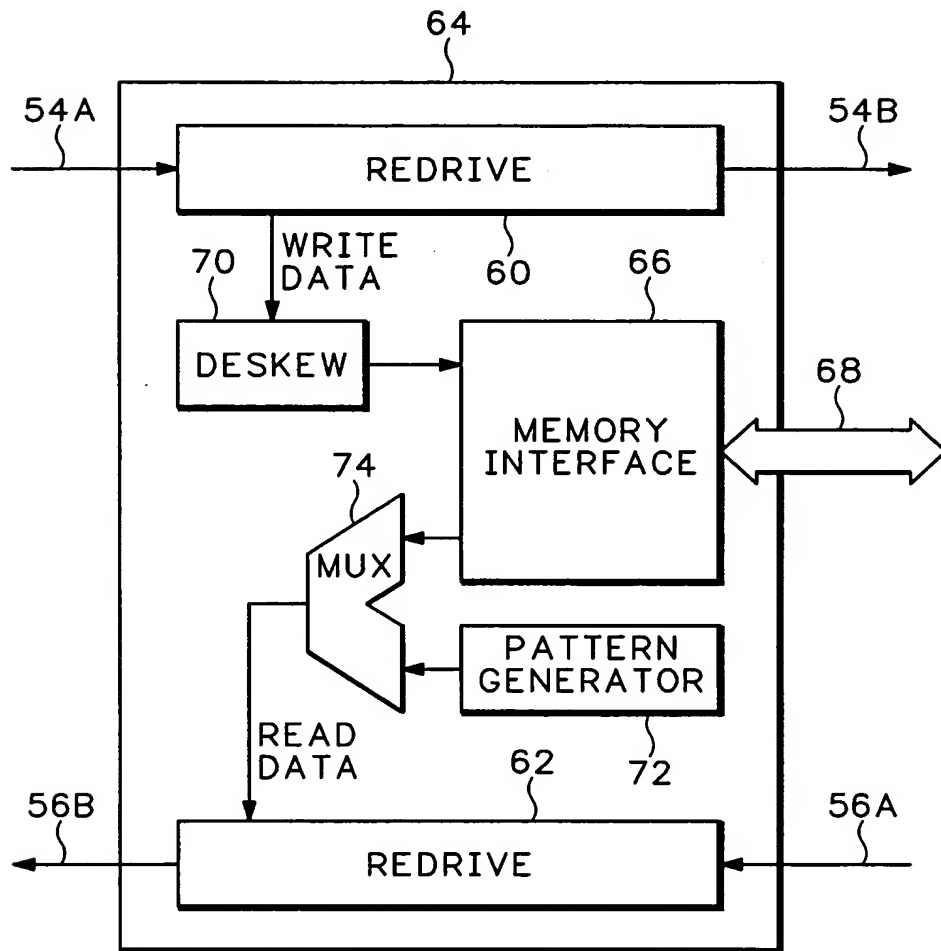
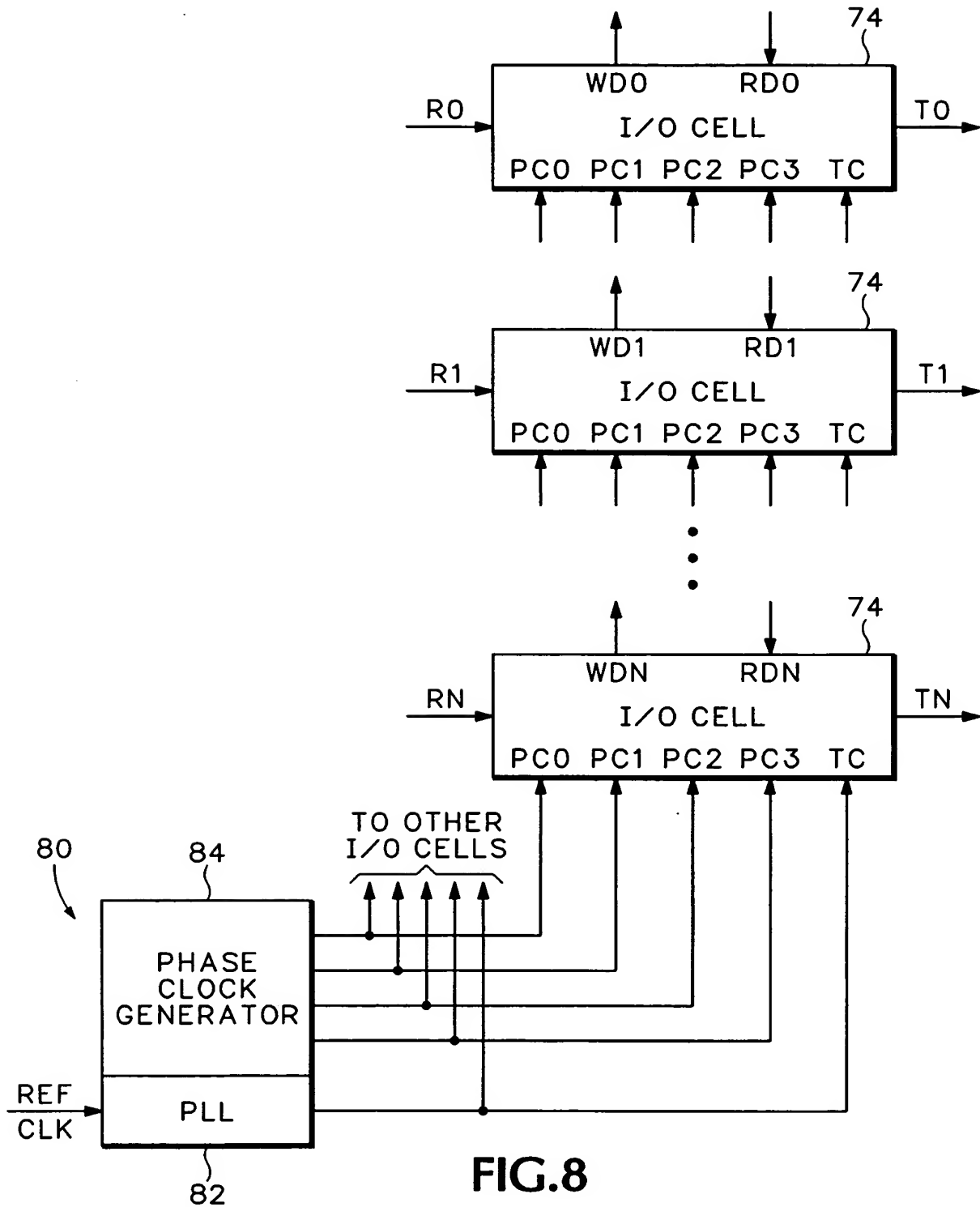
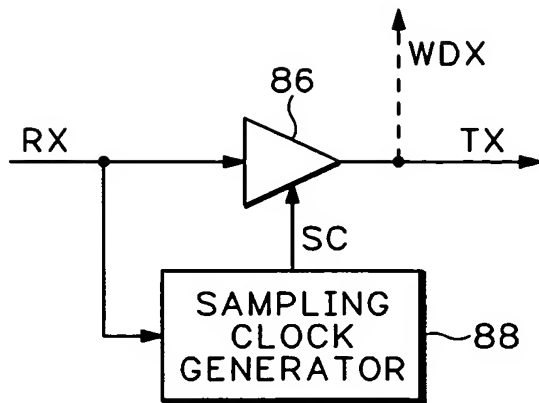


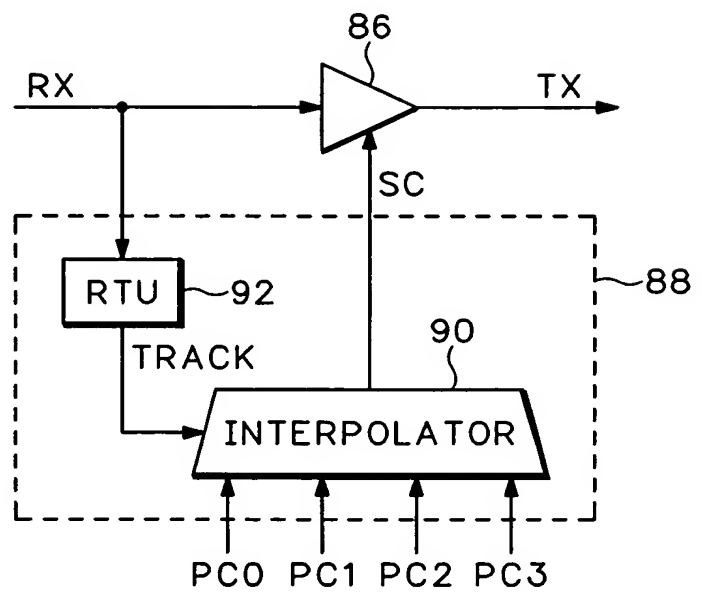
FIG.7

5/20

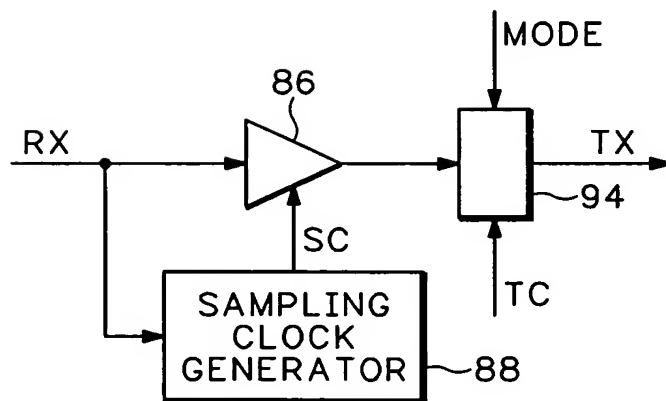




**FIG. 9**



**FIG. 10**



**FIG. 11**

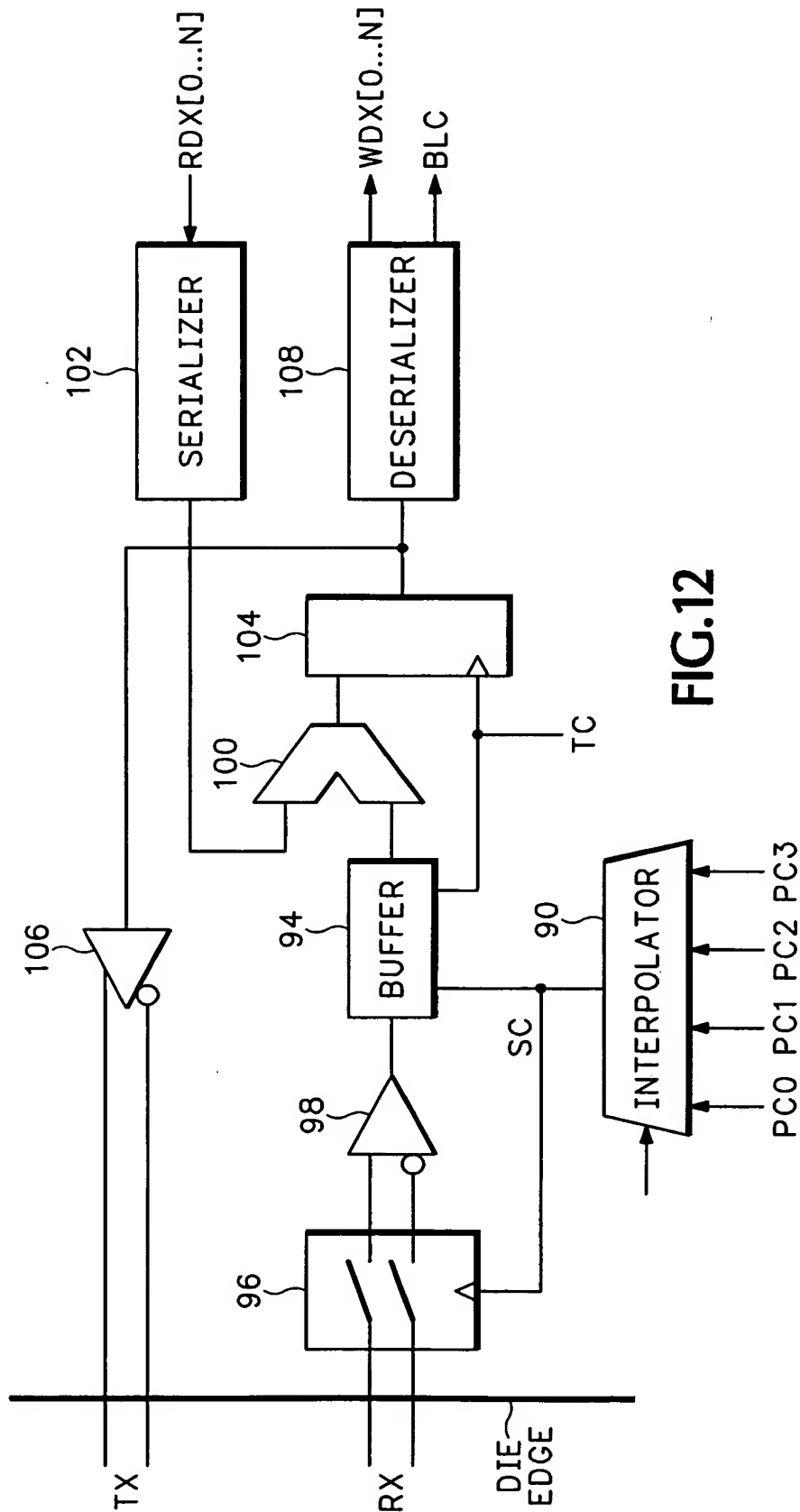


FIG.12

8/20

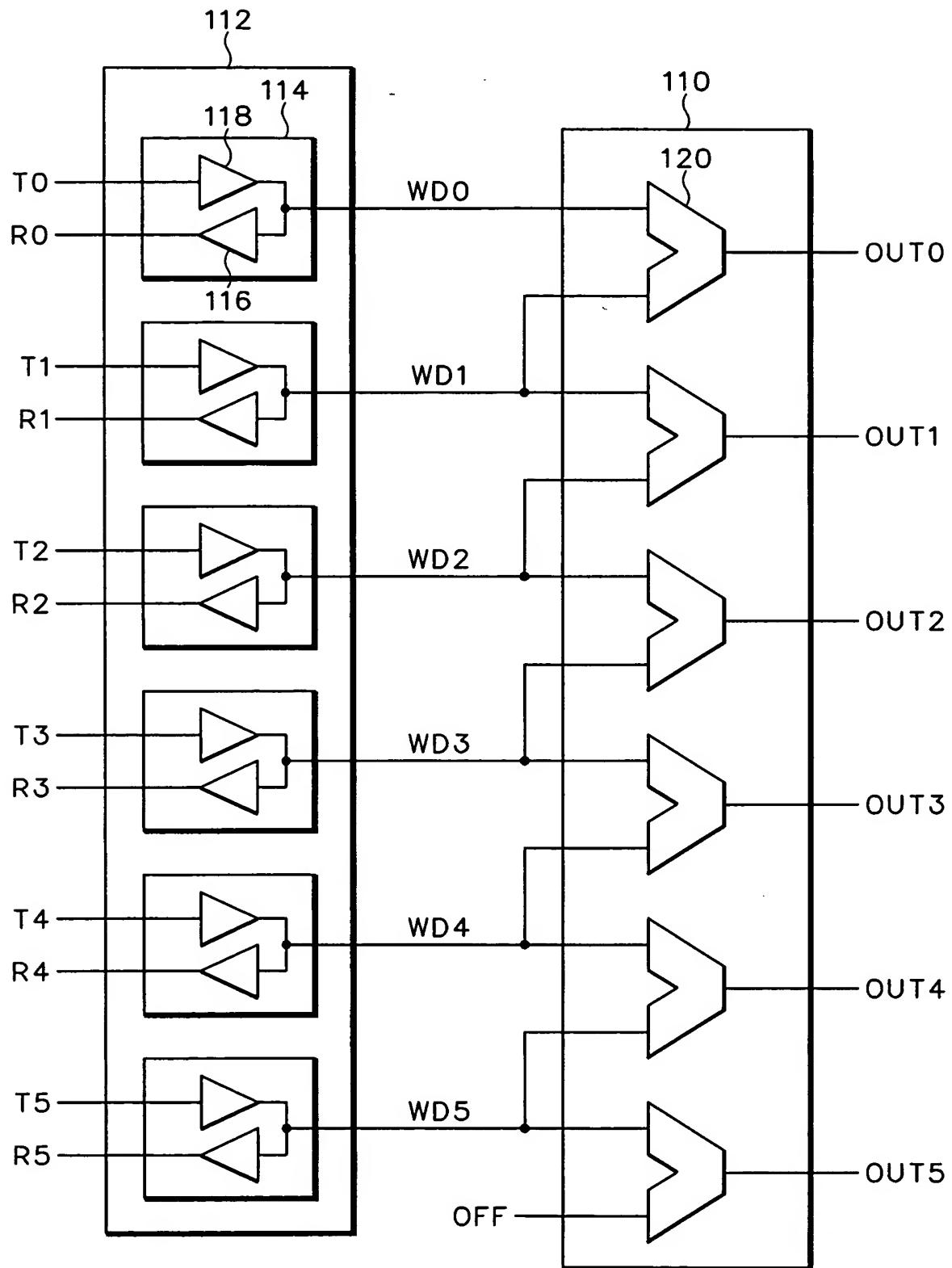
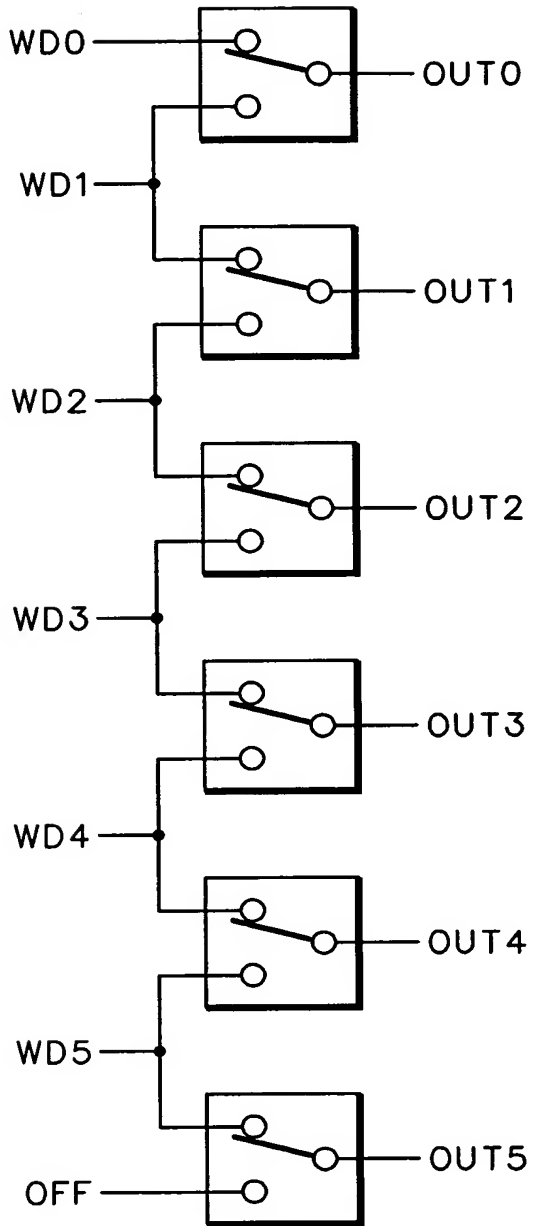


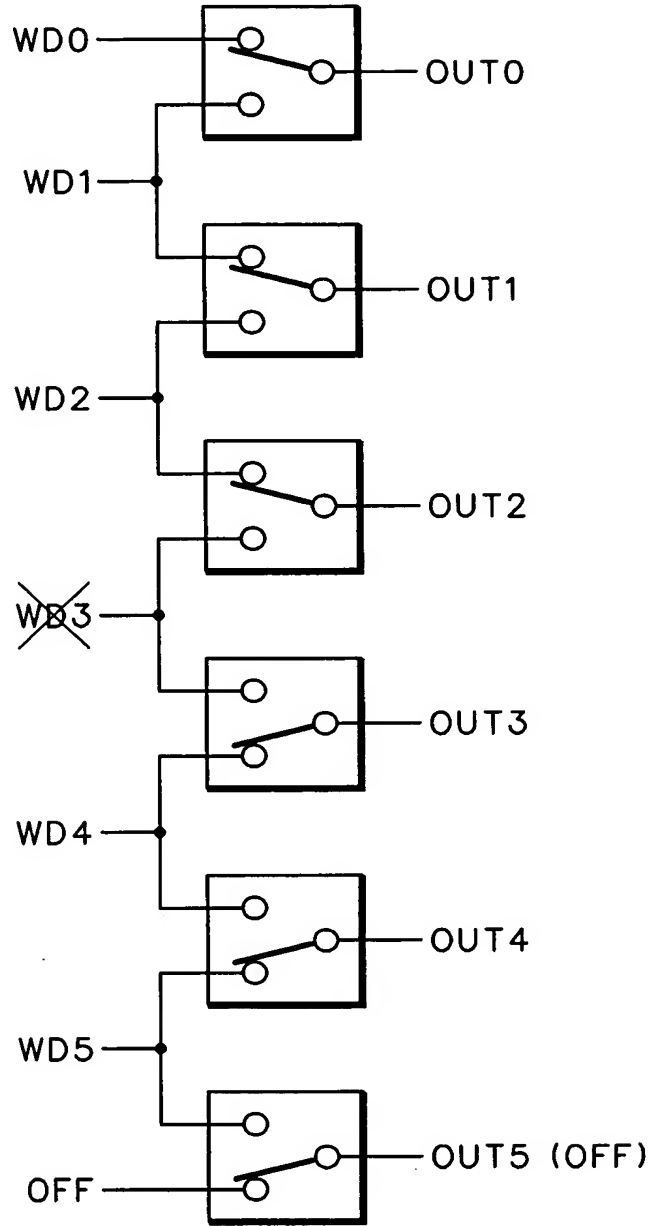
FIG.13



9/20



**FIG.14**



**FIG.15**

10/20

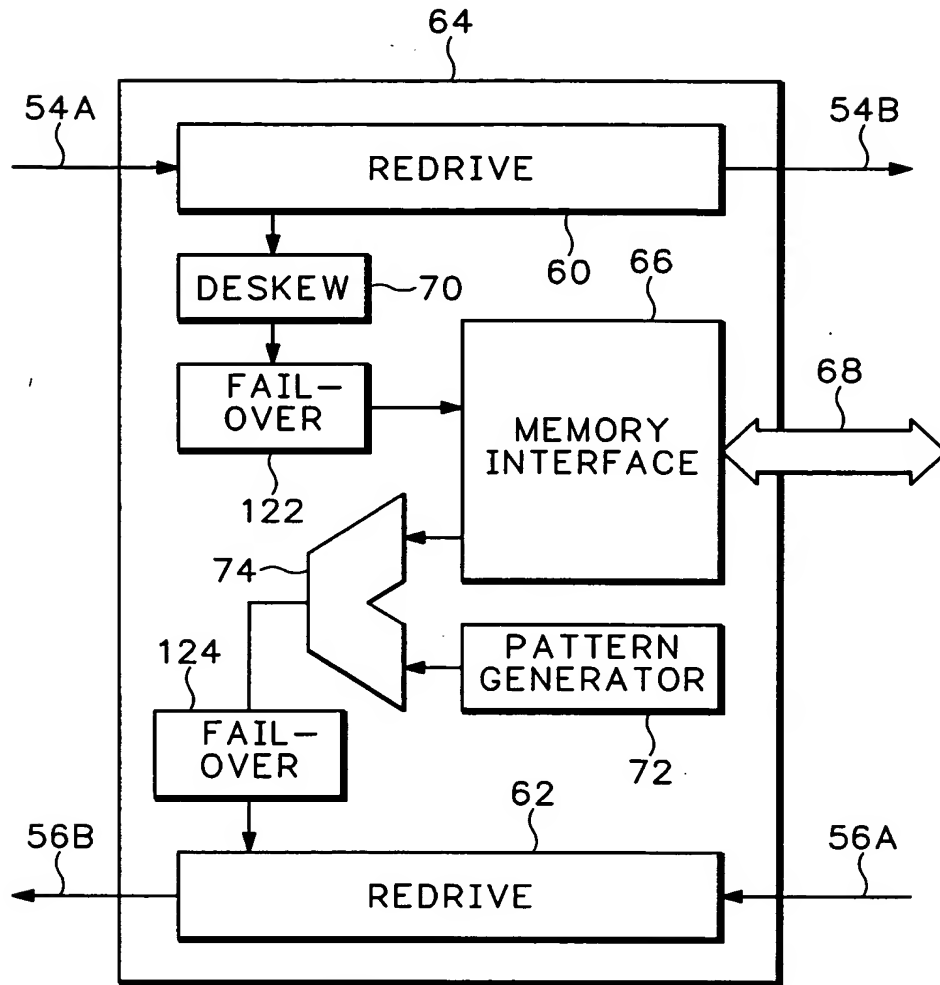


FIG.16

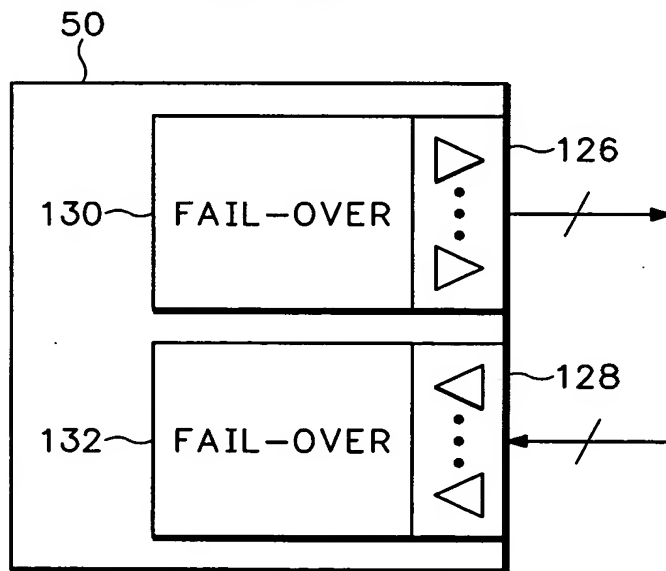


FIG.17

11/20

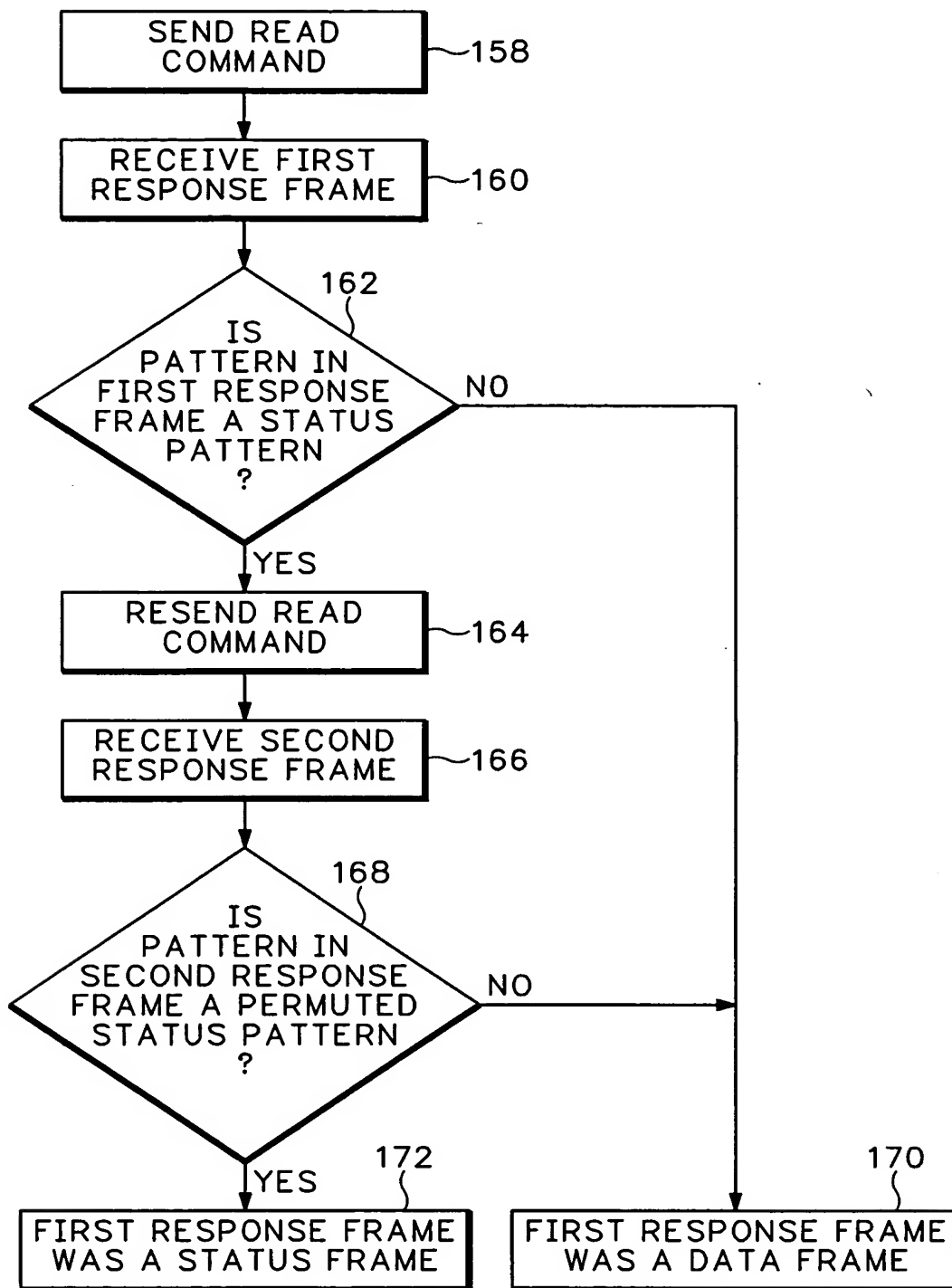
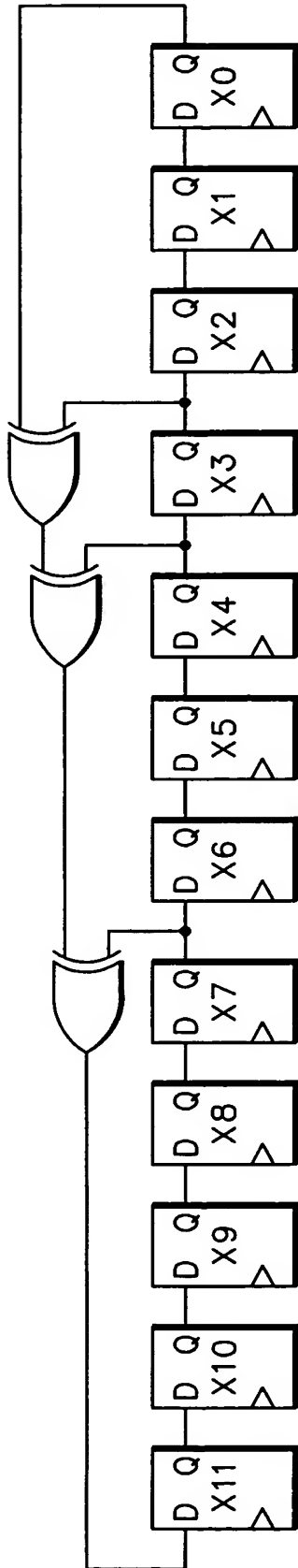


FIG.18



**FIG.19**

[illegible]

**FIG. 20**

TRANSFER \ BIT	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	1	0	0	0	0	0	0	0	0	0	0	0
1	1	1	0	0	0	0	0	0	0	0	0	0	0
2	1	1	0	0	0	0	0	0	0	0	0	0	0
3	1	1	0	0	0	0	0	0	0	0	0	0	0
4	1	1	0	0	0	0	0	0	0	0	0	0	0
5	1	1	0	0	0	0	0	0	0	0	0	0	0
6	1	1	0	0	0	0	0	0	0	0	0	0	0
7	1	1	0	0	0	0	0	0	0	0	0	0	0
8	1	1	0	0	0	0	0	0	0	0	0	0	0
9	1	1	0	0	0	0	0	0	0	0	0	0	0
10	1	1	0	0	0	0	0	0	0	0	0	0	0
11	1	1	0	0	0	0	0	0	0	0	0	0	0

FIG.21

TRANSFER \ BIT	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	1	0	0	0	0	0	0	0	0	0	0
1	0	0	1	0	0	0	0	0	0	0	0	0	0
2	0	0	1	0	0	0	0	0	0	0	0	0	0
3	0	0	1	0	0	0	0	0	0	0	0	0	0
4	0	0	1	0	0	0	0	0	0	0	0	0	0
5	0	0	1	0	0	0	0	0	0	0	0	0	0
6	0	0	1	0	0	0	0	0	0	0	0	0	0
7	0	0	1	0	0	0	0	0	0	0	0	0	0
8	0	0	1	0	0	0	0	0	0	0	0	0	0
9	0	0	1	0	0	0	0	0	0	0	0	0	0
10	0	0	1	0	0	0	0	0	0	0	0	0	0
11	0	0	1	0	0	0	0	0	0	0	0	0	0

FIG.22

TRANSFER \ BIT	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	0	1	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	0	0	0	0
2	0	0	0	1	0	0	0	0	0	0	0	0	0
3	0	0	0	1	0	0	0	0	0	0	0	0	0
4	0	0	0	1	0	0	0	0	0	0	0	0	0
5	0	0	0	1	0	0	0	0	0	0	0	0	0
6	0	0	0	1	0	0	0	0	0	0	0	0	0
7	0	0	0	1	0	0	0	0	0	0	0	0	0
8	0	0	0	1	0	0	0	0	0	0	0	0	0
9	0	0	0	1	0	0	0	0	0	0	0	0	0
10	0	0	0	1	0	0	0	0	0	0	0	0	0
11	0	0	0	1	0	0	0	0	0	0	0	0	0

FIG.23

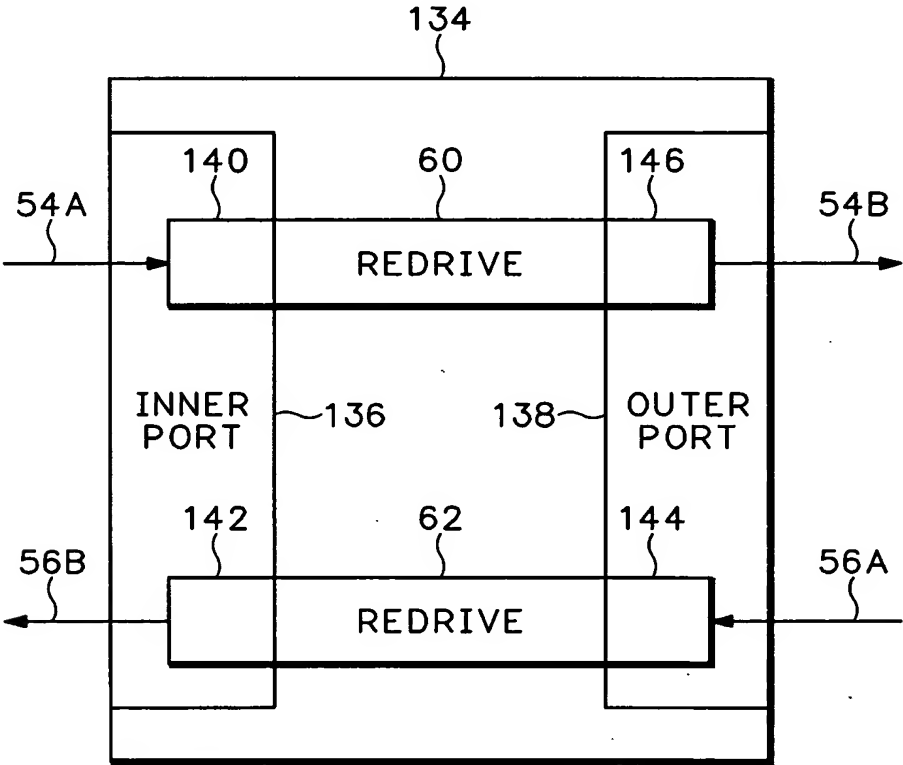
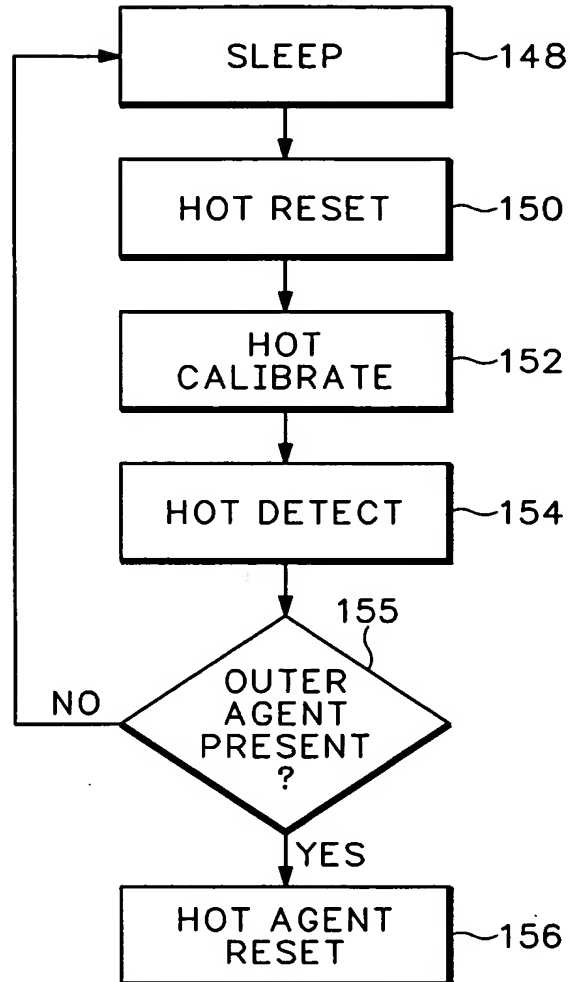
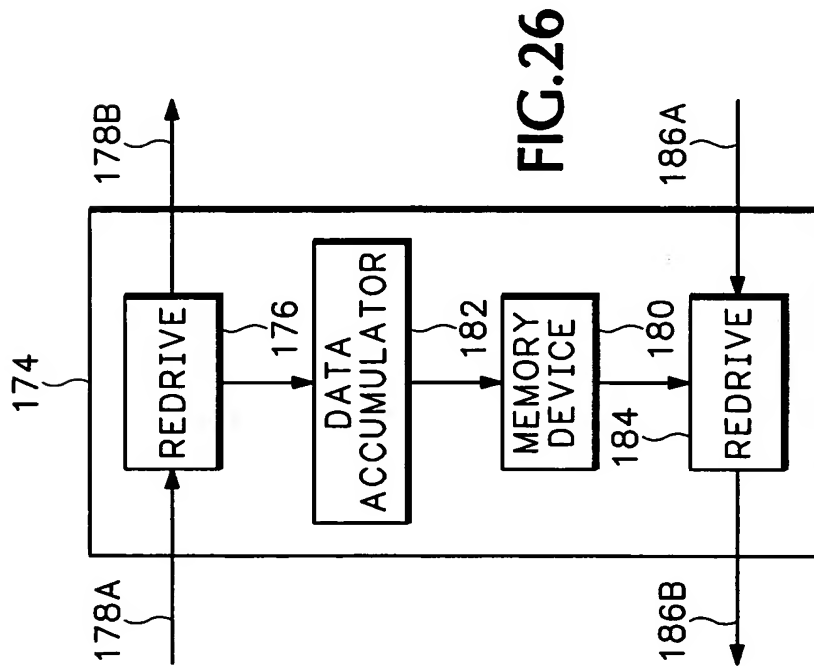
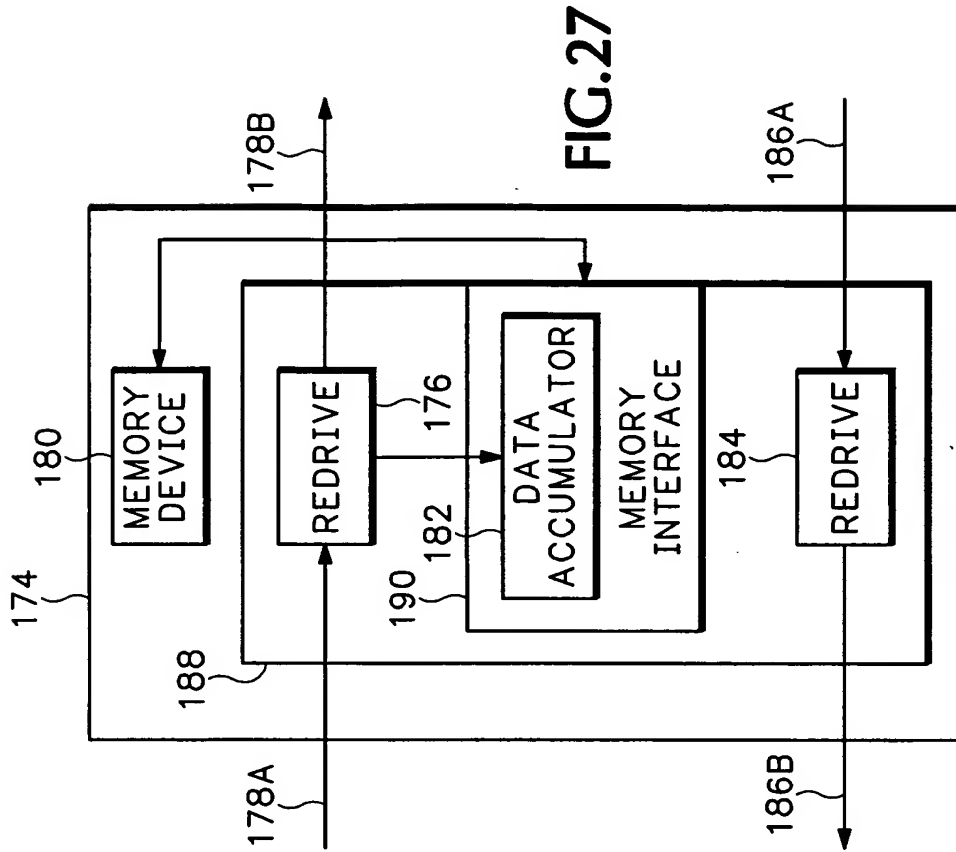


FIG.24



**FIG.25**





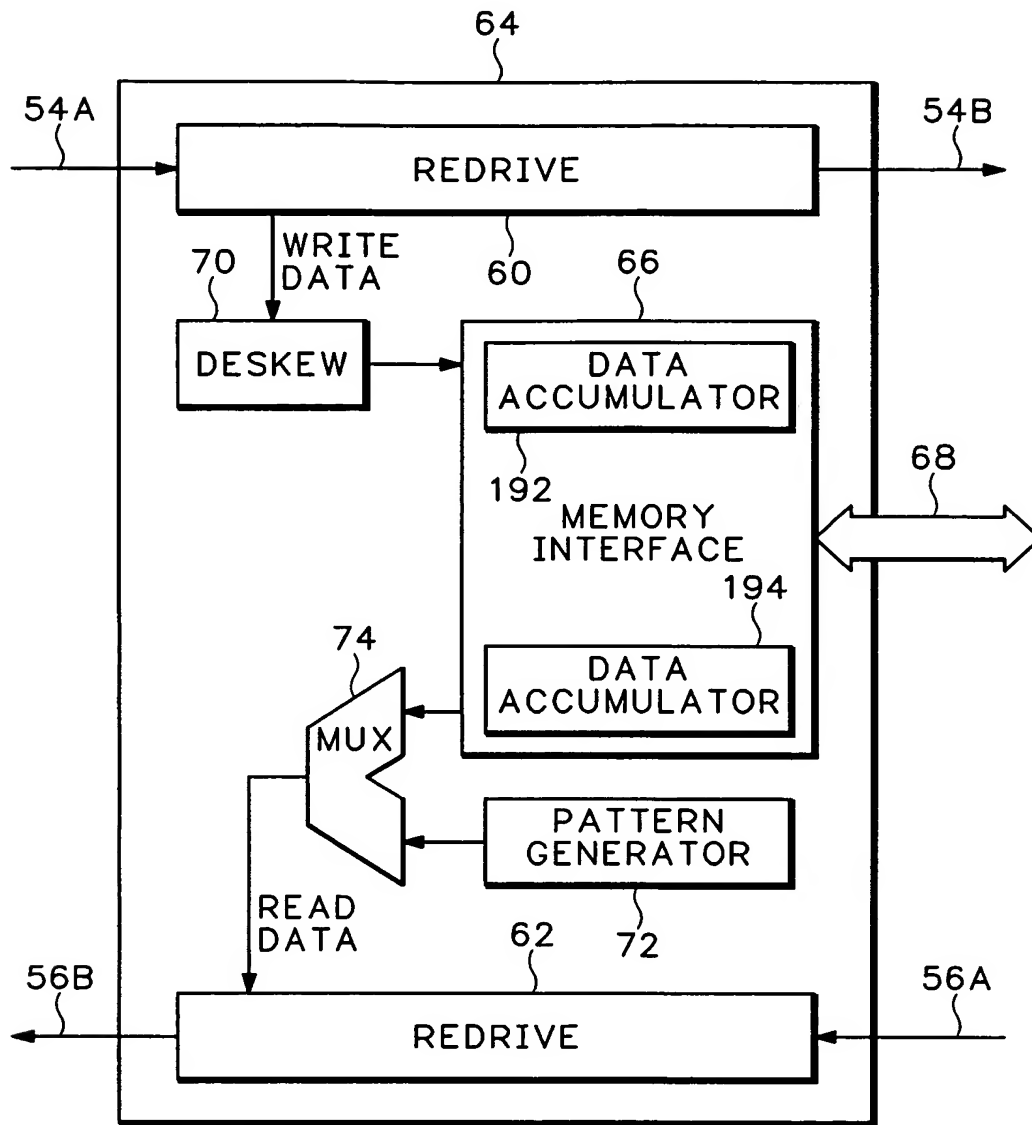


FIG.28

TRANSFER \ BIT	9	8	7	6	5	4	3	2	1	0
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
10										
11										

**FIG.29**

TRANSFER \ BIT	9	8	7	6	5	4	3	2	1	0
0	aE0	aE7	aE8	F0	aC20	aC16	aC12	aC8	aC4	aC0
1	aE1	aE6	aE9	F1	aC21	aC17	aC13	aC9	aC5	aC1
2	aE2	aE5	aE10	aE13	aC22	aC18	aC14	aC10	aC6	aC2
3	aE3	aE4	aE11	aE12	aC23	aC19	aC15	aC11	aC7	aC3
4	FE21	B8	B7	B6	B5	B4	B3	B2	B1	B0
5	FE20	B9	B10	B11	B12	B13	B14	B15	B16	B17
6	FE19	B26	B25	B24	B23	B22	B21	B20	B19	B18
7	FE18	B27	B28	B29	B30	B31	B32	B33	B34	B35
8	FE17	B44	B43	B42	B41	B40	B39	B38	B37	B36
9	FE16	B45	B46	B47	B48	B49	B50	B51	B52	B53
10	FE15	B62	B61	B60	B59	B58	B57	B56	B55	B54
11	FE14	B63	B64	B65	B66	B67	B68	B69	B70	B71

FE0	FE7	FE8	
FE1	FE6	FE9	
FE2	FE5	FE10	FE13
FE3	FE4	FE11	FE12

**FIG.31**

TRANSFER \ BIT	9	8	7	6	5	4	3	2	1	0
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
10										
11										

FRAME N-1

TRANSFER \ BIT	9	8	7	6	5	4	3	2	1	0
0										
1										
2										
3										
4										
5										
6										
7										
8										
9										
10										
11										

FRAME N

FIG.30

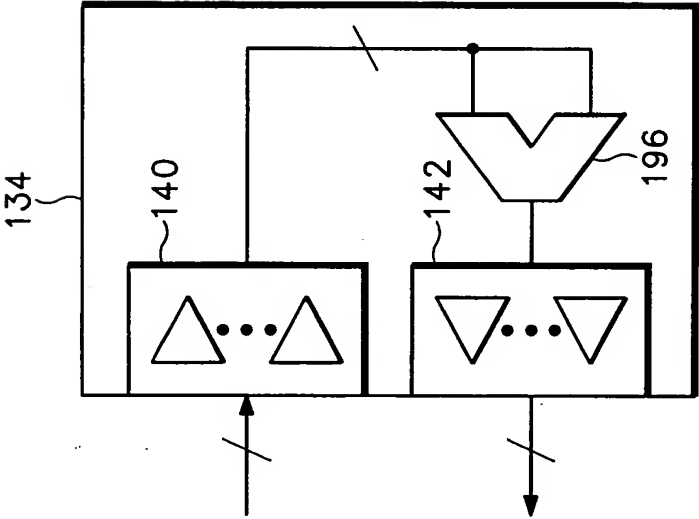


FIG. 32

TRANSFER BIT LANE													13	12	11	10	9	8	7	6	5	4	3	2	1	0	MAPPING "A"	MAPPING "B"
RECEIVE BIT LANE													3	2	1	0	4	3	2	1	0	4	3	2	1	0		
RECEIVE BIT LANE													8	7	6	5	9	8	7	6	5	9	8	7	6	5		

FIG. 33